MANTIQIY QURILMALARNI DASTURLASHDA VHDL DASTURLASH TILIDAN FOYDALANISH USULLARI

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ANNOTATSIYA

Maqolada Quartus II muhitida Verilog HDL dasturlash tili yordamida mantiqiy qurilmalarni dasturlash va boshqarish usullari haqida fikr yuritiladi. Qurilma sifatida Altera Cyclone III 3C16 FPGA ni tanlaymiz. Qurilmaning pinlari va portlarini dastur o’zgaruvchilariga ulash orqali ularga uzatiluvchi signallarni o’zgartirish va boshqarish usullari yoritiladi.

Kalit so'zlar: integral mikrosxema, Verilog HDL File, Assignment, 7 segmentli displey, pinlar, segmentlar.

HOW TO USE VHDL PROGRAMMING LANGUAGE IN PROGRAMMING LOGICAL DEVICES

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ABSTRACT

The article discusses how to program and control logical devices using the Verilog HDL programming language in the Quartus II environment. We choose Altera Cyclone III 3C16 FPGA as the device. By connecting the pins and ports of the device to the program changers, they are taught how to change and control the signals transmitted to them.

Keywords: integrated circuits, Verilog HDL file, Assignment, Pins, 7 segment display, segments.